

TRANSFER SWITCH SCHEMATIC

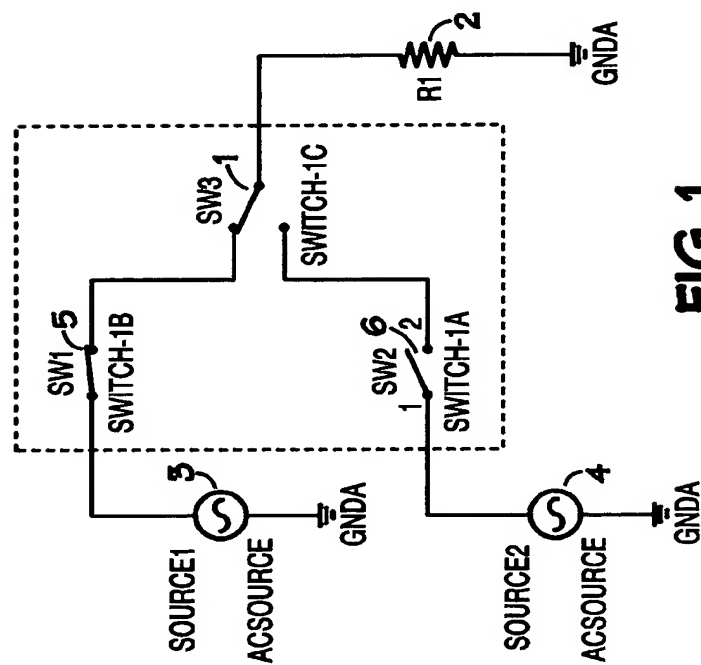
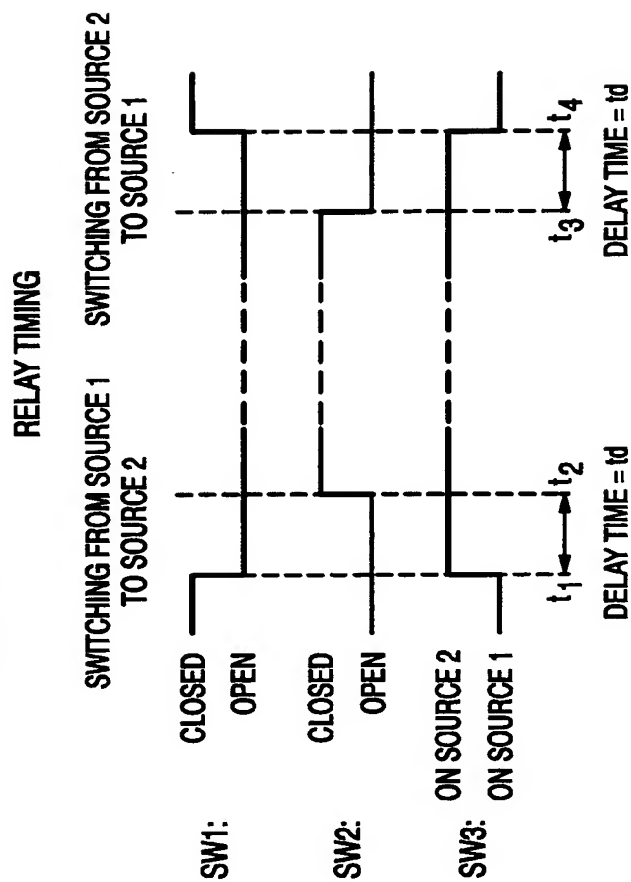


FIG.1

FIG.2



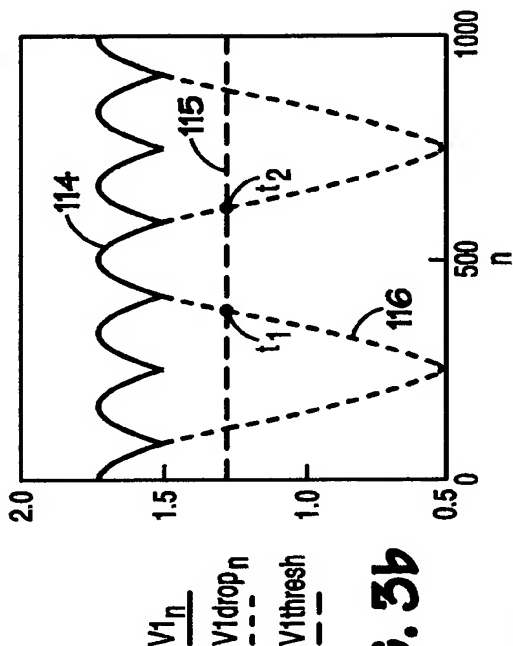


Fig. 3b

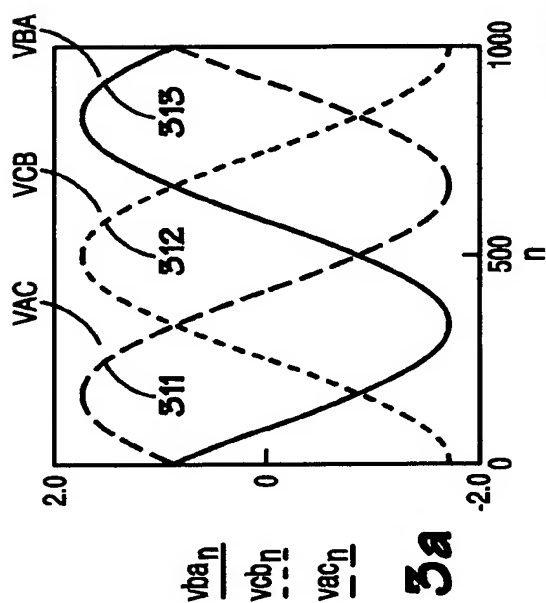


FIG. 3a

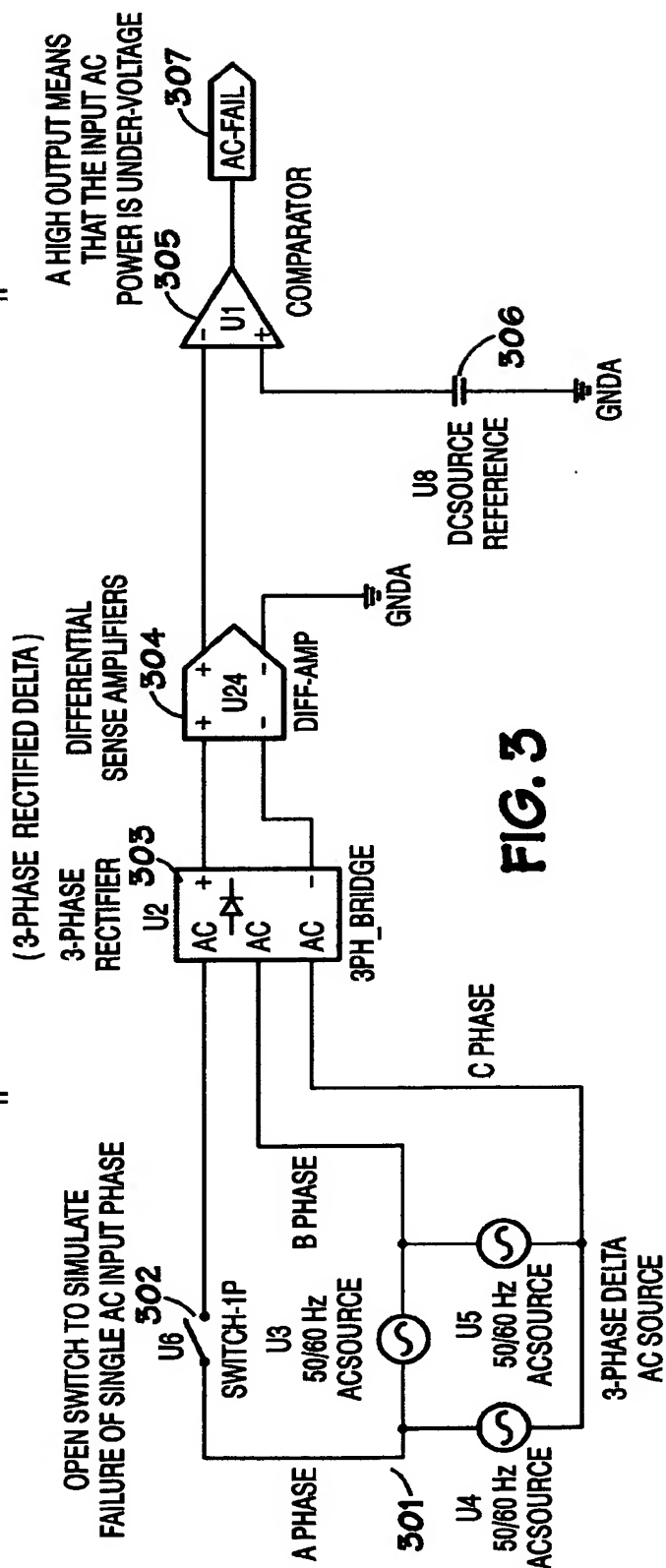
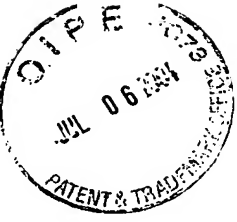


Fig. 5



4

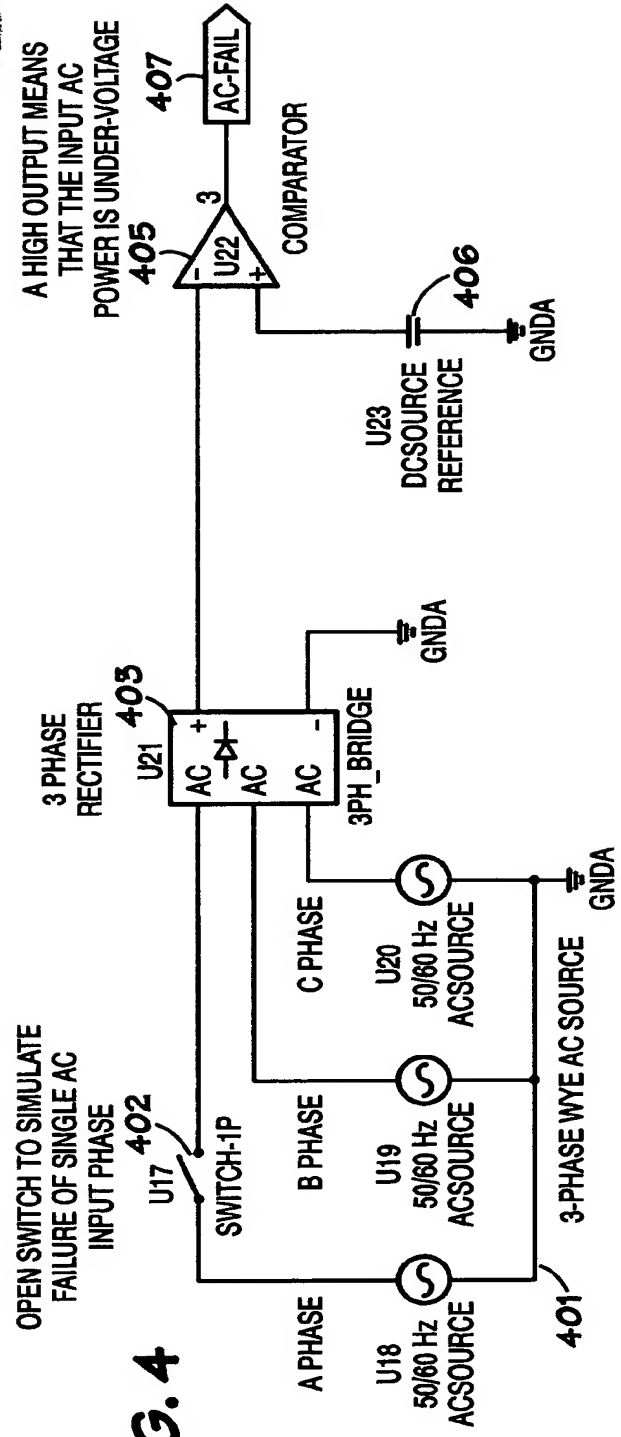


FIG. 4

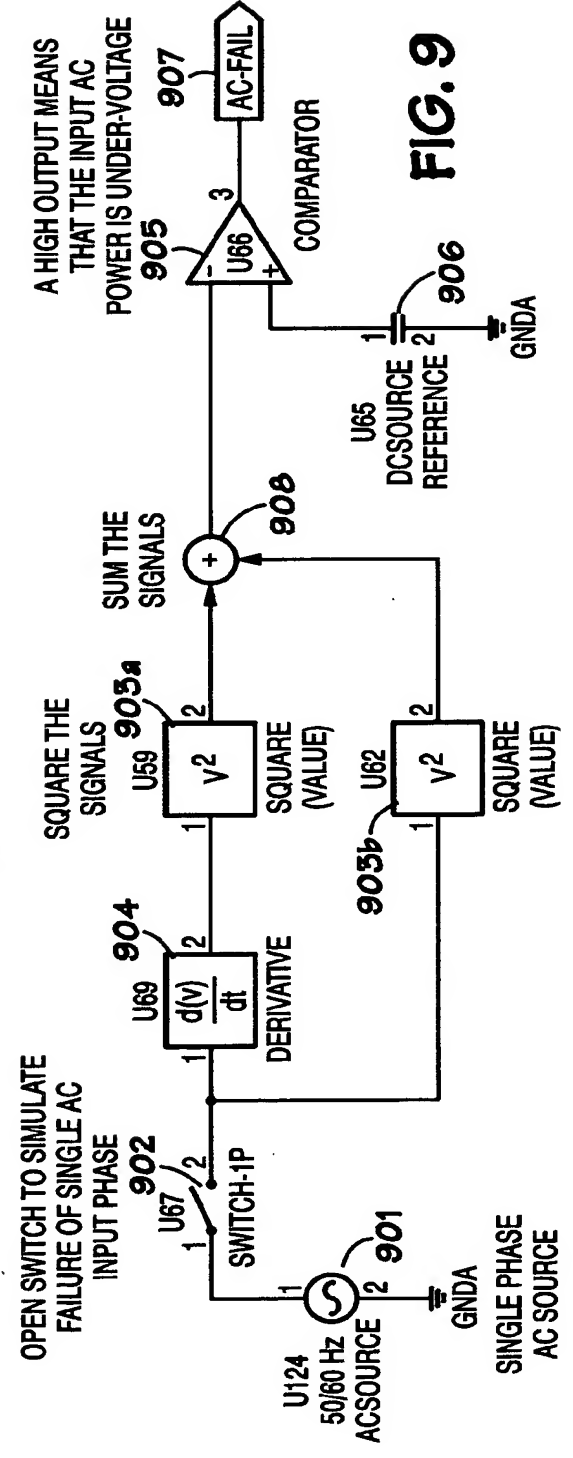


FIG. 9

4

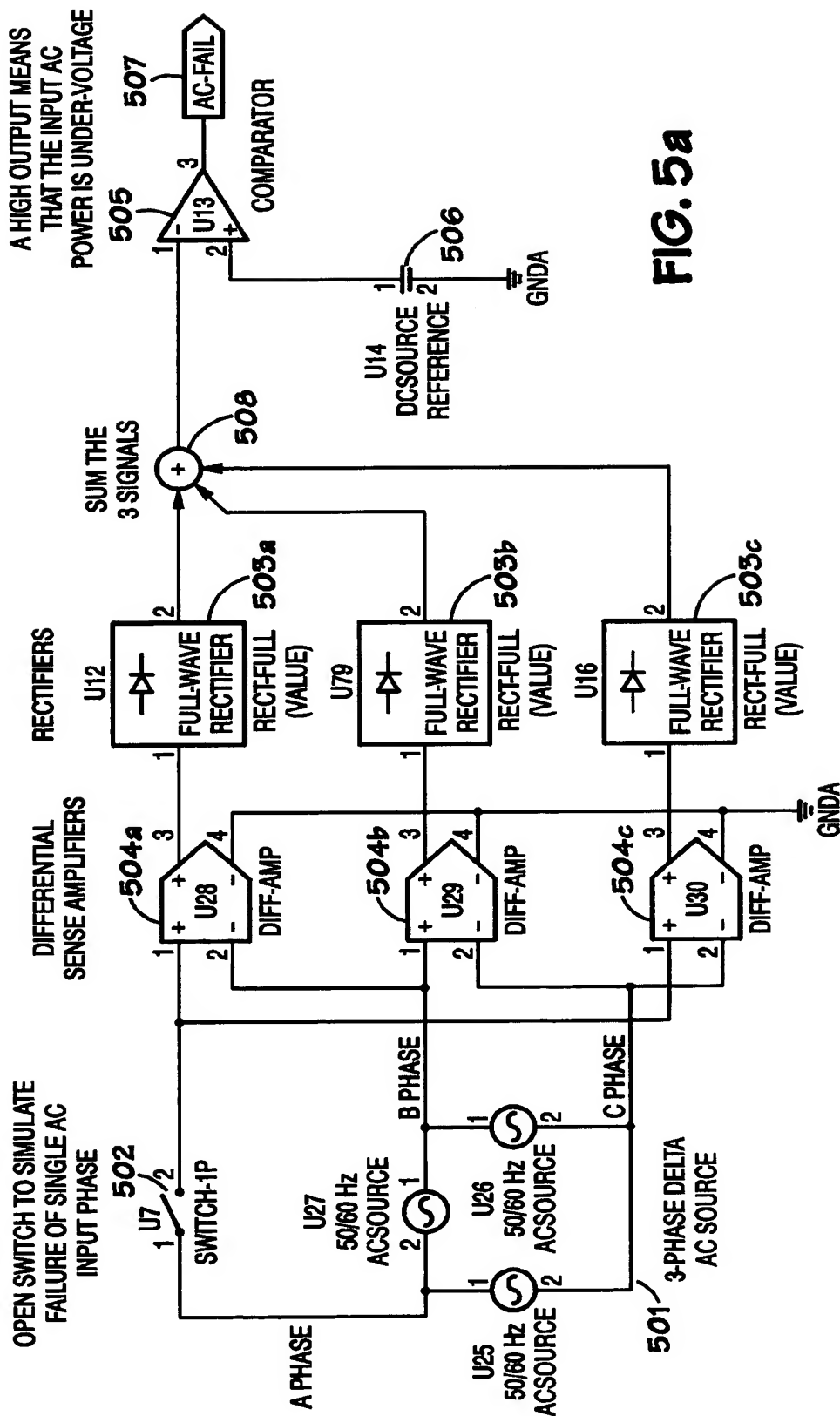
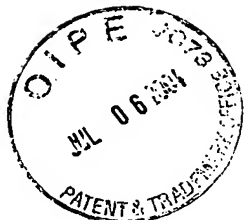


FIG. 5a

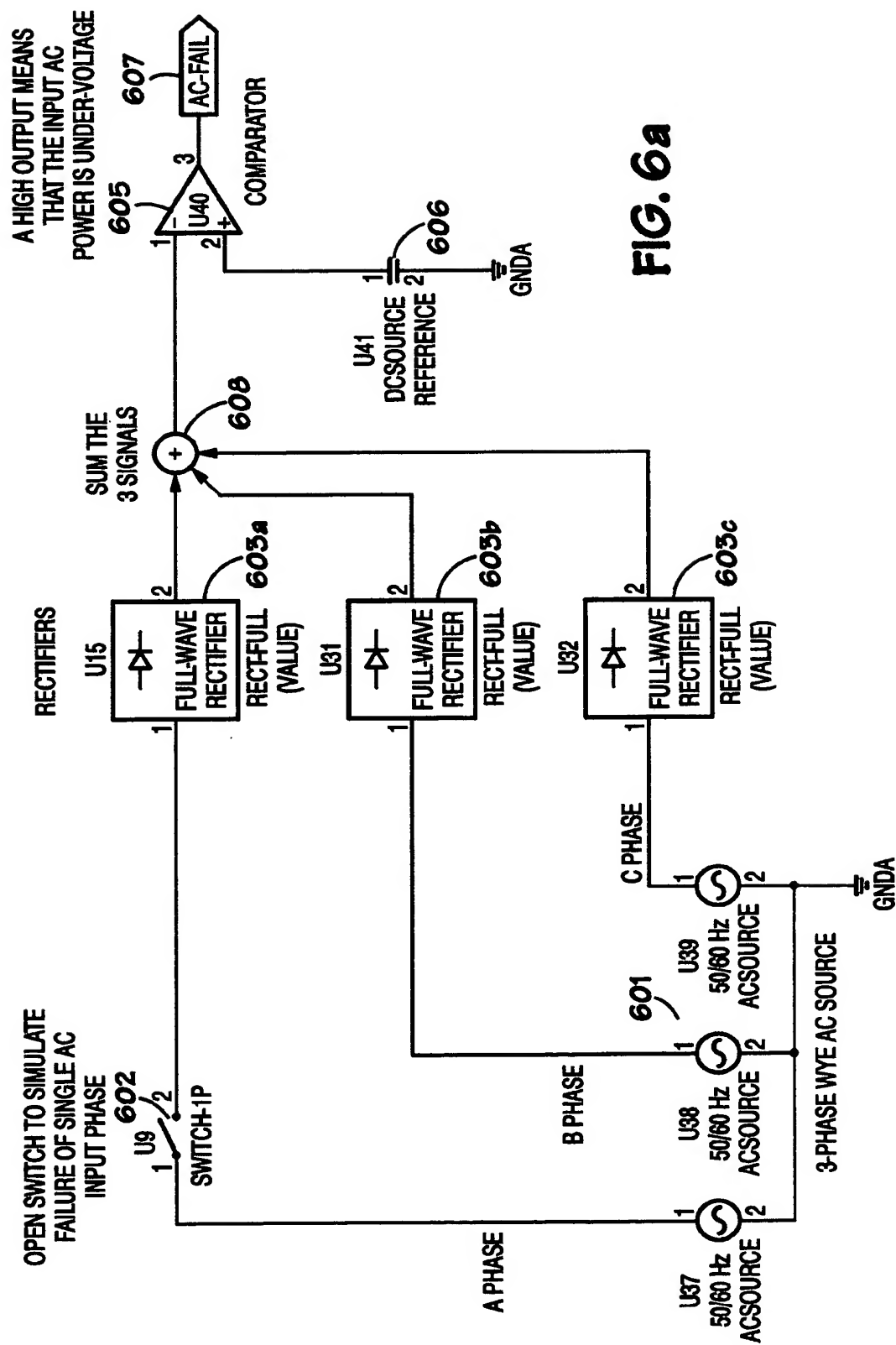
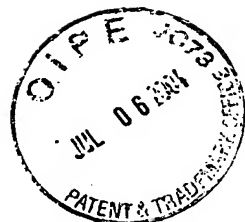


FIG. 6a



4

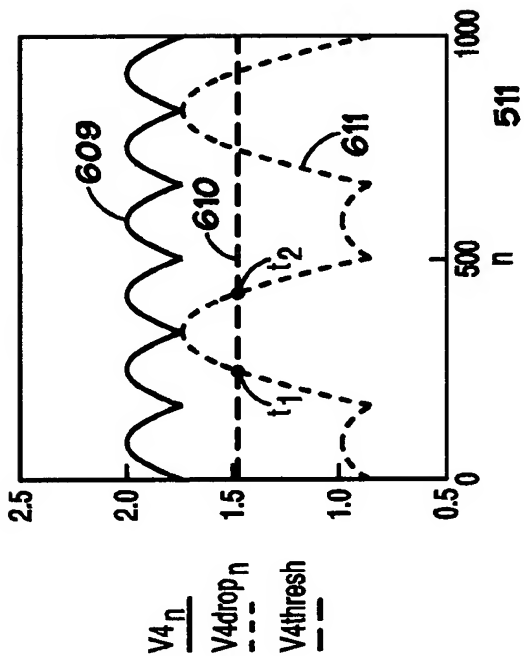


FIG. 6b

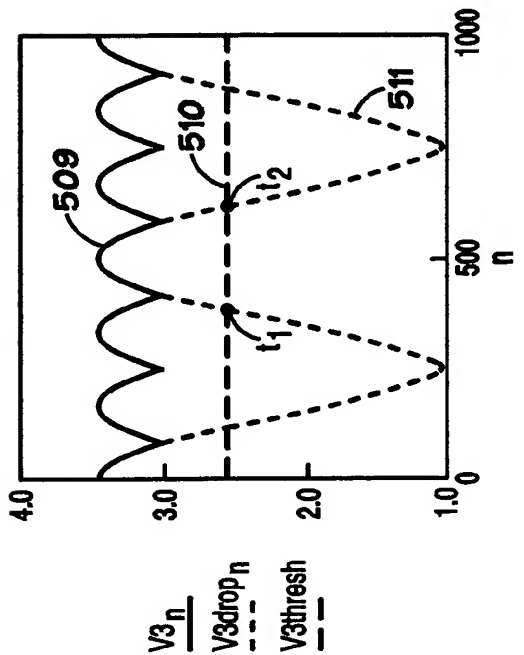
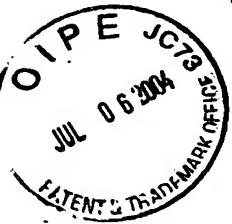


FIG. 5b



4

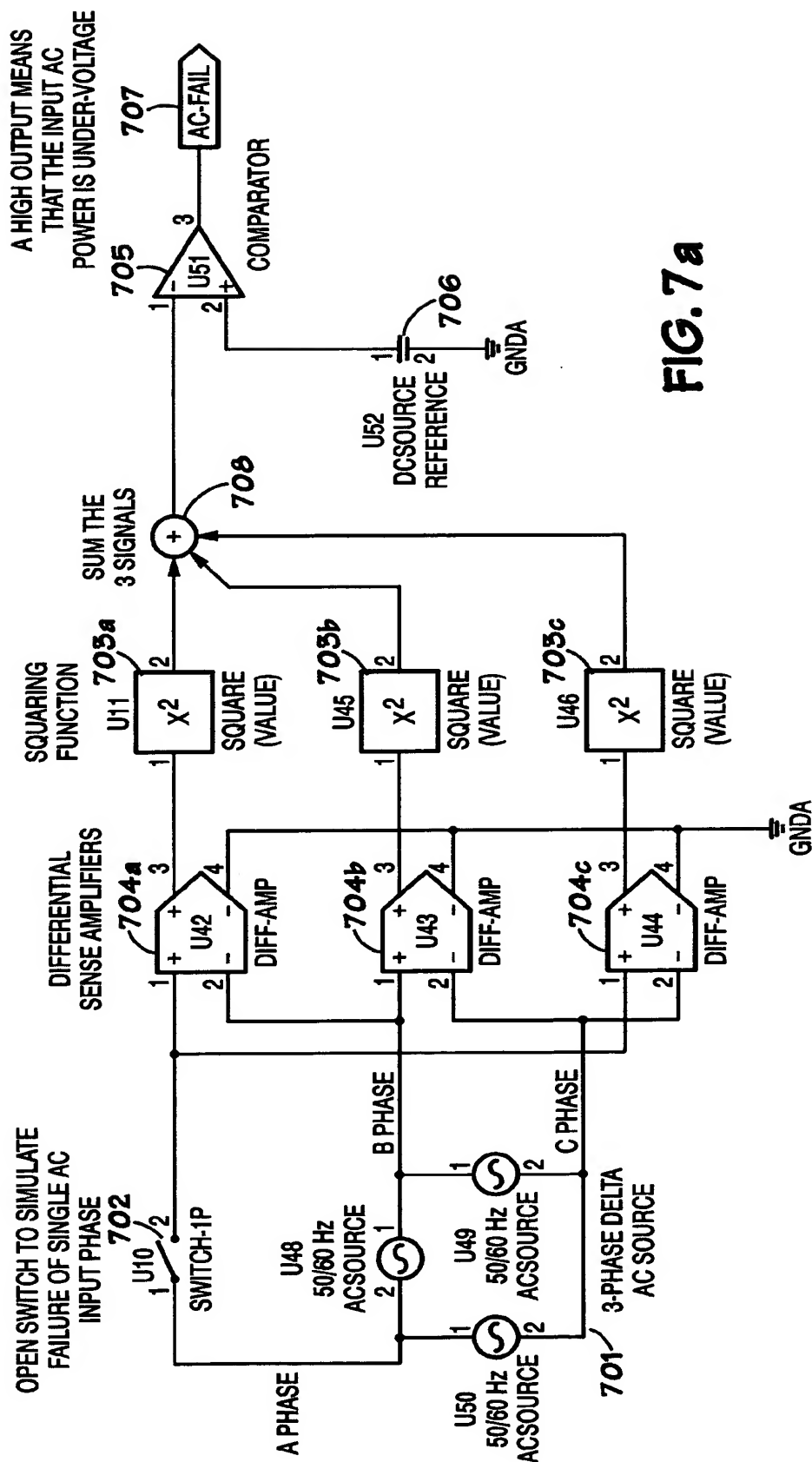
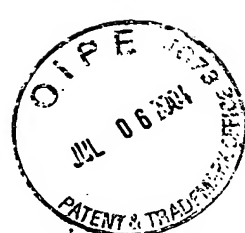


FIG. 7a



4

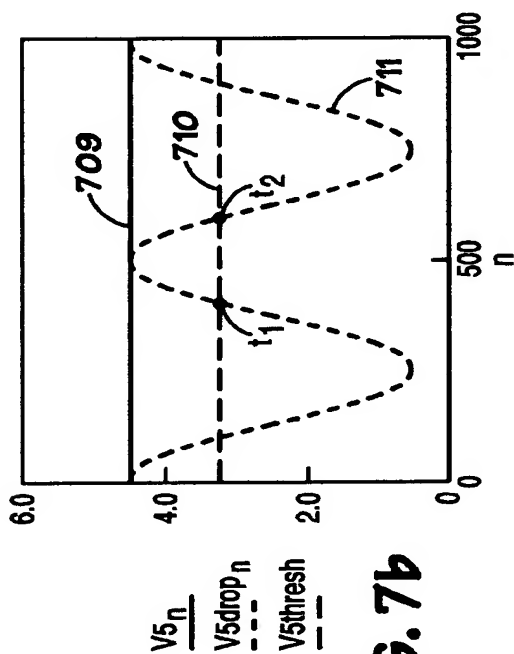


FIG. 7b

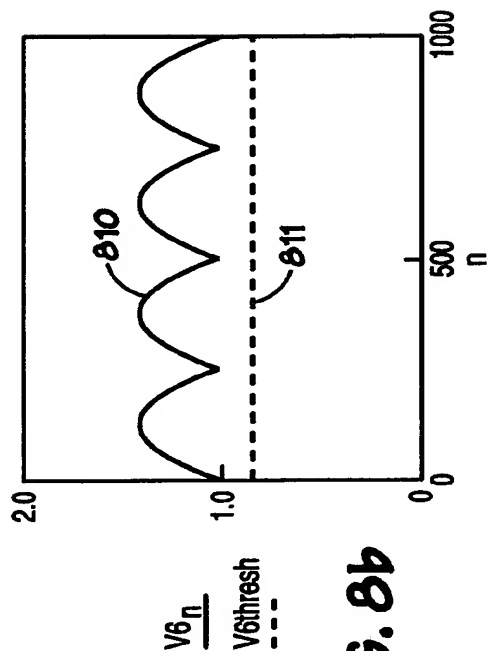
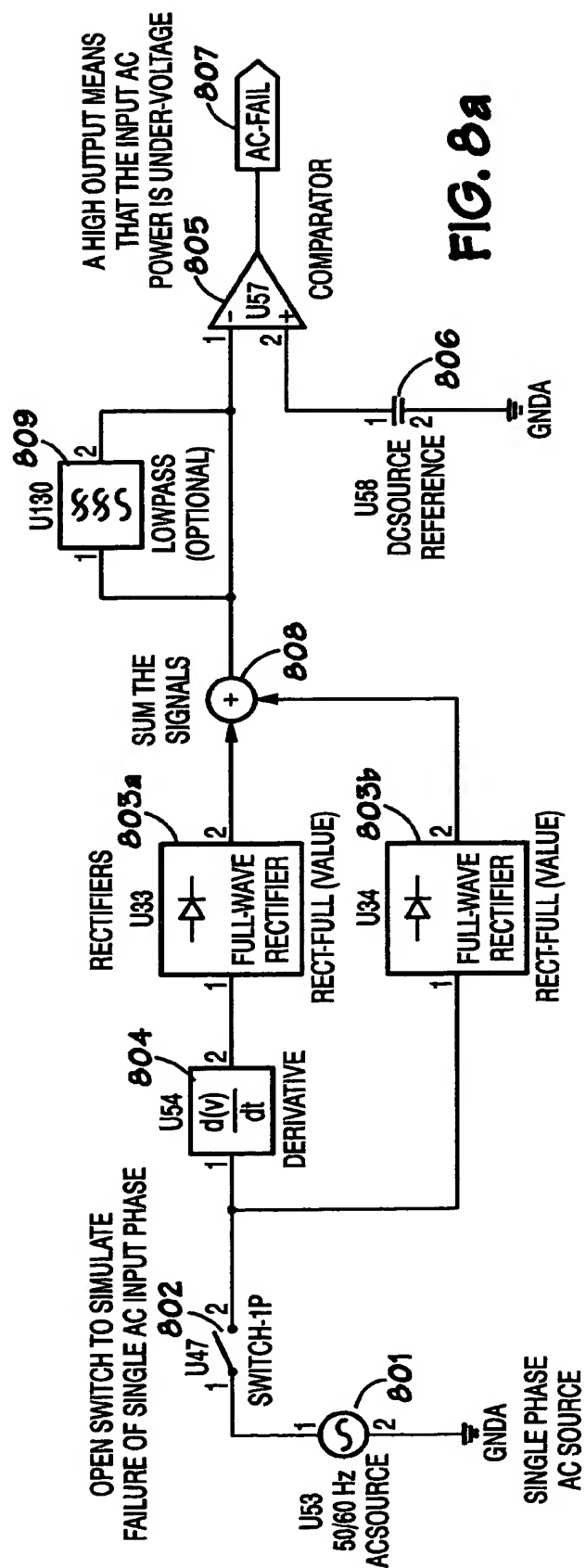


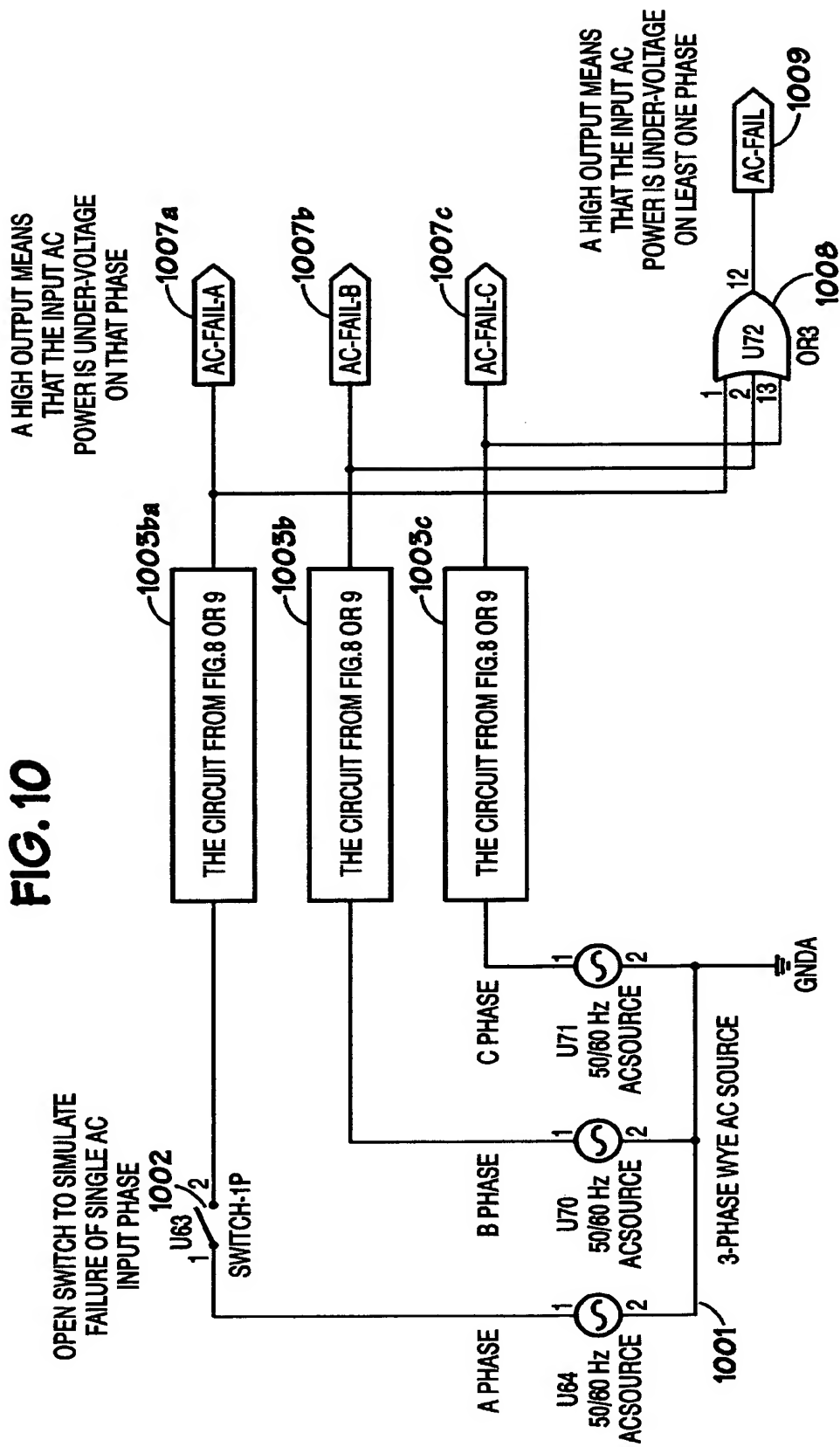
FIG. 8b

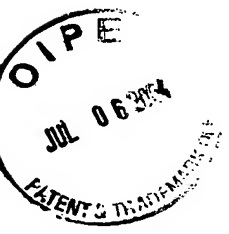
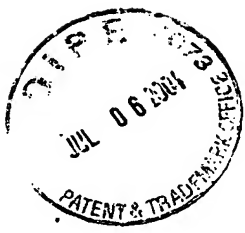


4



FIG. 10





4

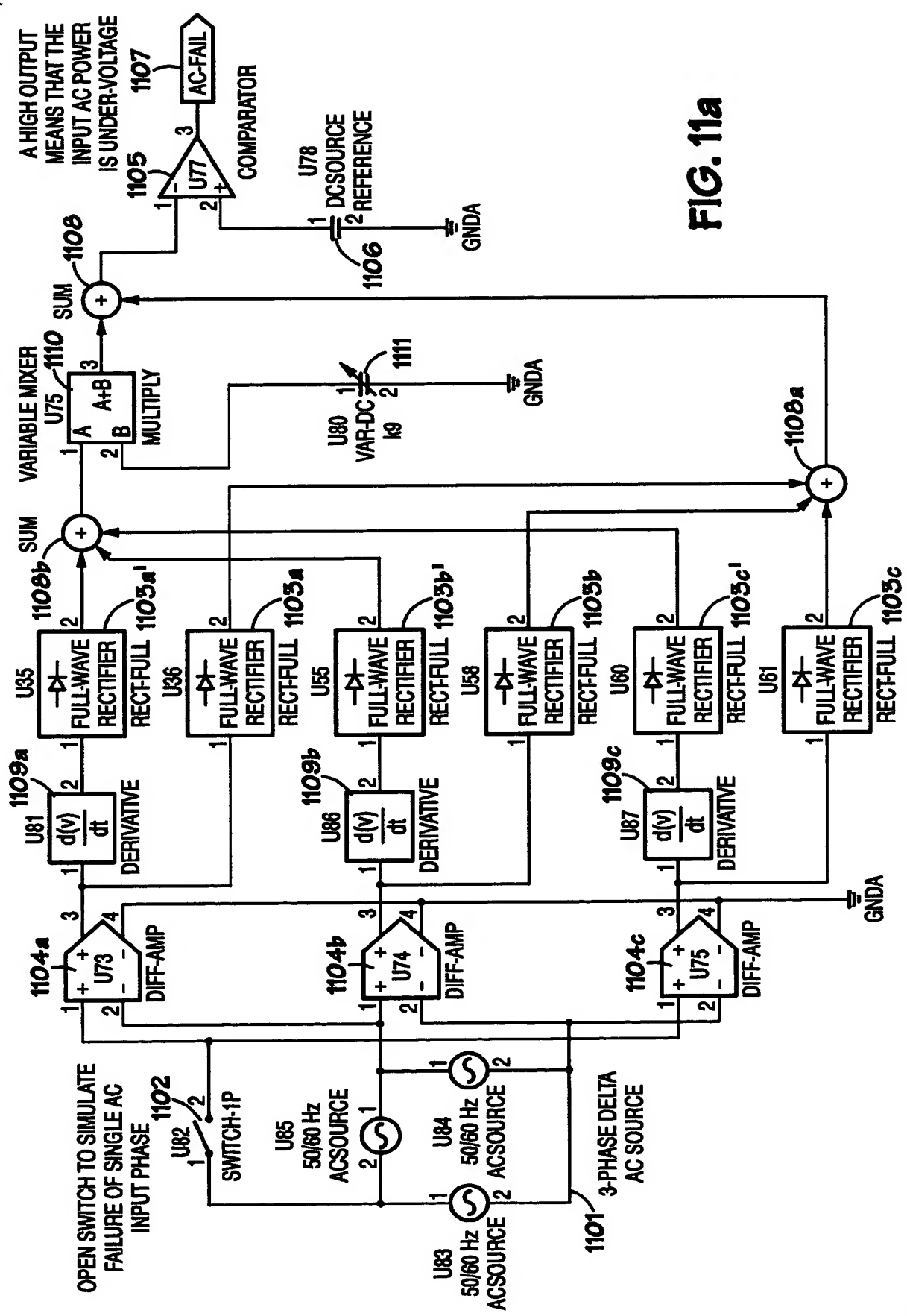
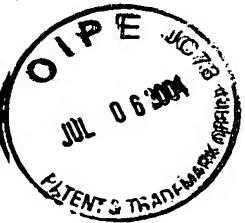


FIG. 11a



4

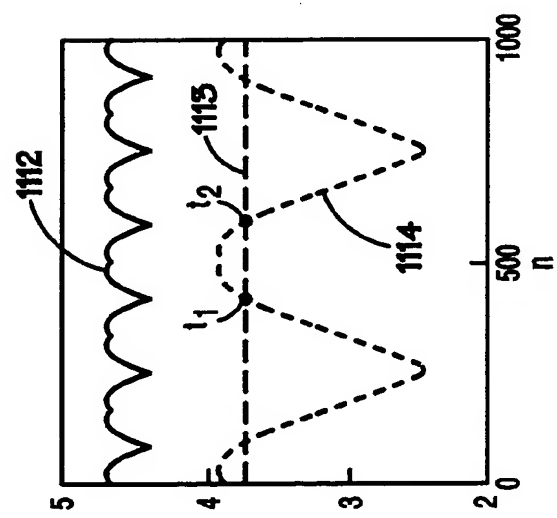


FIG. 11b

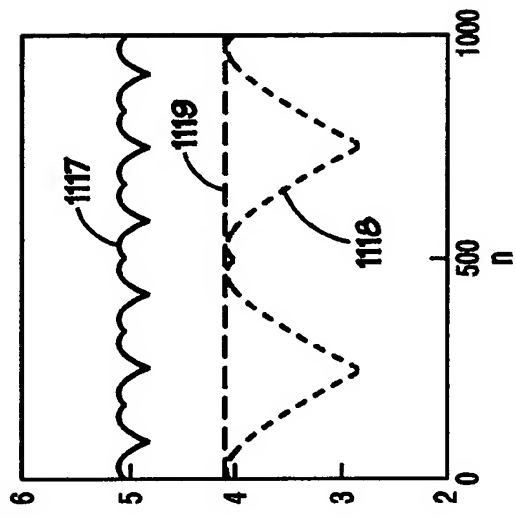


FIG. 11c

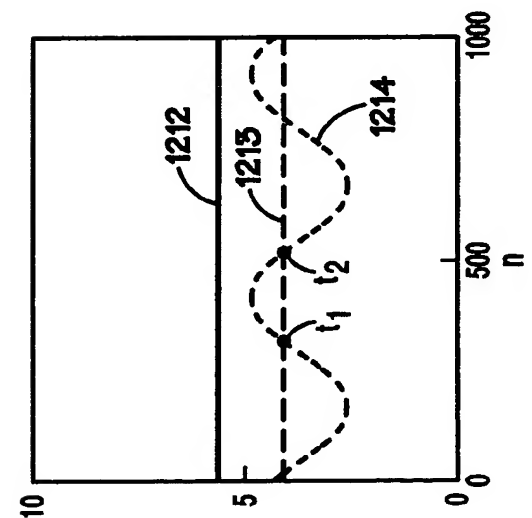


FIG. 12b

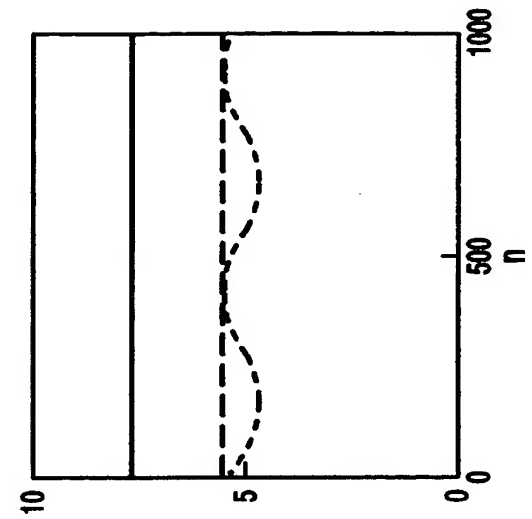
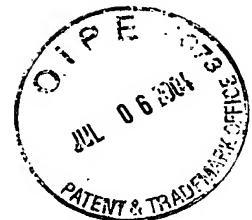


FIG. 12c

4



4

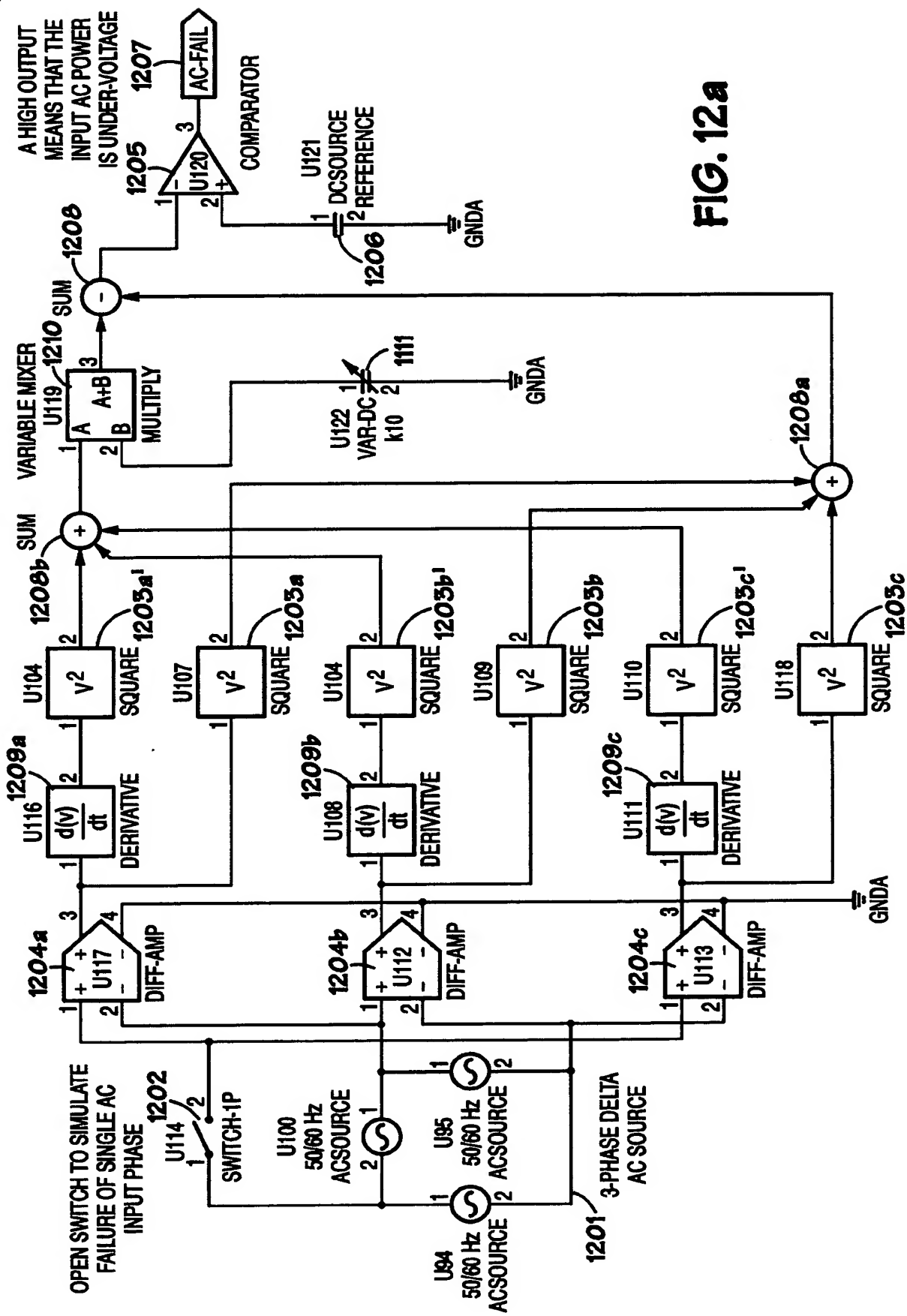
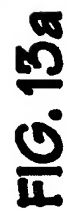
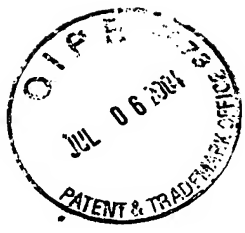


FIG. 12a





4

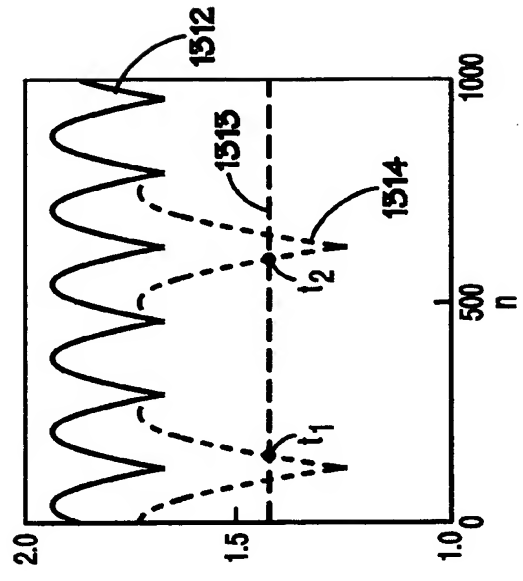


FIG. 13b

$V11_n$
 $V11_{dropn}$
 $V11_{thresh}$

